HALOGEN FREE

GREEN

(5-2008)



vPolyTanTM Polymer Surface-Mount Chip Capacitors, Molded Case, High Performance Type



LINKS TO ADDITIONAL RESOURCES



PERFORMANCE / ELECTRICAL CHARACTERISTICS

Operating Temperature: -55 °C to +125 °C (above 105 °C, voltage derating is required)

Capacitance Range: 10 μ F to 330 μ F

Capacitance Tolerance: \pm 20 %

Voltage Rating: 2.5 V_{DC} to 35 V_{DC}

FEATURES

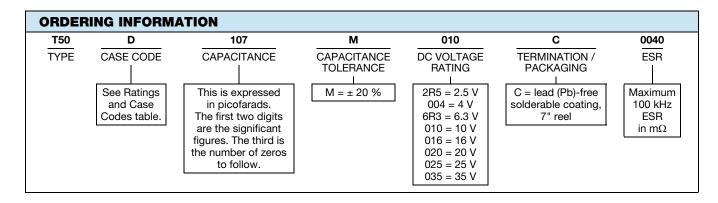
- Operating temperature range of -55 °C to +125 °C
- High temperature and high humidity operation
- Qualification testing based on AEC-Q200 with 85 °C / 85 % RH performance testing up to 500 hours

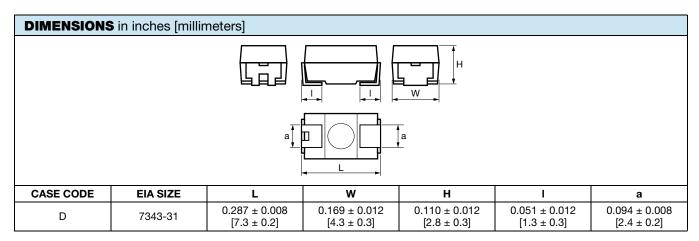


- Molded case 7343-31 EIA size
- Terminations: Ni / Pd / Au
- Moisture sensitivity level 3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

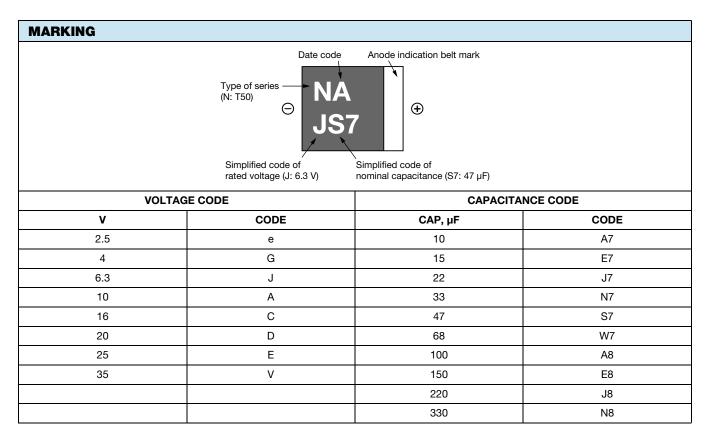
- · Decoupling, smoothing, filtering
- Switch mode and point of load power supply
- · Automotive infotainment and cockpit electronics
- · Storage and networking infrastructure equipment
- Industrial applications requiring high temperature operation





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RATINGS A	RATINGS AND CASE CODES								
μF	2.5 V	4.0 V	6.3 V	10 V	16 V	25 V	35 V		
10							D		
33					D	D			
47					D				
100				D	D				
150			D						
220	D	D	D	D					
330	D	D	D						



DATE	DATE CODE											
YEAR						МО	NTH					
TEAR	1	2	3	4	5	6	7	8	9	10	11	12
2018	N	Р	Q	R	S	Т	U	V	W	Х	Υ	Z
2019	а	b	С	d	е	f	g	h	j	k	I	m
2020	n	р	q	r	s	t	u	V	w	х	У	z
2021	Α	В	С	D	Е	F	G	Н	J	K	L	М

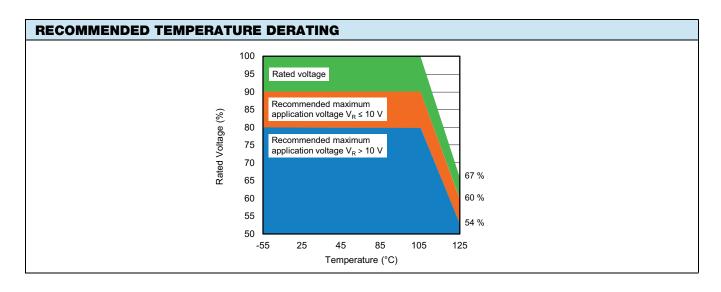
Note

• Marking code repeats every four years in alphabetical order (letter of I, i, O, and o are excluded)



STANDARD	RATING	S					
CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. DCL AT 25 °C (μΑ)	MAX. DF AT 25 °C 120 Hz (%)	MAX. ESR AT 25 °C 100 kHz (mΩ)	MAX. RIPPLE AT 45 °C 100 kHz I _{RMS} (A)	HIGH TEMPERATURE LOAD, TIME (h)
		2.5 V _{DC}	AT +105 °C; 1.7	7 V _{DC} AT 125	°C		
220	D	T50D227M2R5C0025	55.0	10	25	3.00	2000
330	D	T50D337M2R5C0025	82.5	10	25	3.00	2000
		4 V _{DC} /	AT +105 °C; 2.7	V_{DC} AT 125 °	С		
220	D	T50D227M004C0025	88.0	10	25	3.00	2000
330	D	T50D337M004C0025	132.0	10	25	3.00	2000
		6.3 V _D	_C AT +105 °C; 4	V_{DC} AT 125 °	С		
150	D	T50D157M6R3C0025	94.5	10	25	3.00	2000
220	D	T50D227M6R3C0025	138.6	10	25	3.00	2000
330	D	T50D337M6R3C0025	207.9	10	25	3.00	2000
		10 V _D	AT +105 °C; 7	V _{DC} AT 125 °	С		
100	D	T50D107M010C0040	100.0	10	40	2.37	2000
220	D	T50D227M010C0040	220.0	10	40	2.37	2000
		16 V _{DC}	AT +105 °C; 10	V _{DC} AT 125 °	C		
33	D	T50D336M016C0070	52.8	10	70	1.79	1000
47	D	T50D476M016C0070	75.2	10	70	1.79	1000
100	D	T50D107M016C0050	160.0	10	50	2.12	1000
		25 V _{DC}	AT +105 °C; 17	V_{DC} AT 125 °	C		
33	D	T50D336M025C0060	82.5	10	60	1.93	1000
33	D	T50D336M025C0100	82.5	10	100	1.50	1000
•		35 V _{DC}	AT +105 °C; 23	V_{DC} AT 125 °	С	•	
10	D	T50D106M035C0120	35.0	10	120	1.36	1000

RECOMMENDED VOLTAGE DERATING GUIDELINES							
CAPACITOR VOLTAGE RATING AT -55 °C TO +105 °C	CAPACITOR CATEGORY VOLTAGE AT +105 °C TO +125 °C	RECOMMENDED VOLTAGE DERATING AT -55 °C TO +105 °C	RECOMMENDED VOLTAGE DERATING AT +105 °C TO +125 °C				
2.5	1.7	2.3	1.5				
4	2.7	3.6	2.4				
6.3	4	5.7	3.6				
10	7	9	6.3				
16	10	12.8	8.6				
25	17	20	13.6				
35	23	28	18.9				





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POWER DISSIPATION	
CASE CODE	MAXIMUM PERMISSIBLE POWER DISSIPATION (W) AT \leq +45 °C IN FREE AIR
D	0.225

STANDARD PACKAGING QUANTITY					
CASE CODE	UNITS PER 7" REEL				
D	500				

PERFORMANC	E CHARACTERISTICS				
ITEM	CONDITION	POST TEST PERFORMANCE			
	2000 h, rated voltage applied at 105 °C	Capacitance change	Within ± 20 % of initial value		
Endurance	2000 h, 2/3 rated voltage applied at 125 °C	Dissipation factor	Within initial limits		
Endurance	(for < 16 V parts) 1000 h, 2/3 rated voltage applied at 125 °C	Leakage current	Shall not exceed 300 % of initial limit		
	(for ≥ 16 V parts)	ESR	Shall not exceed 300 % of initial limit		
	. ,	Capacitance change	Within ± 20 % of initial value		
Shelf life test	2000 h no voltage applied at 105 °C	Dissipation factor	Within initial limits		
	1000 h no voltage applied at 125 °C	Leakage current	Shall not exceed 300 % of initial limit		
		ESR	Shall not exceed 300 % of initial limit		
		Capacitance change	-5 % to +50 % of initial value (≤ 4 V) -5 % to +40 % of initial value (≥ 6.3 V)		
Humidity test	500 h, rated voltage applied at 85 °C / 85 % RH	Dissipation factor	Within initial limit		
•		Leakage current	Shall not exceed 300 % of initial limit		
		ESR	Shall not exceed 300 % of initial limit		
		Capacitance change	Within ± 30 % of initial value		
	-55 °C	Dissipation factor	Within initial limit		
		Leakage current	n/a		
		Capacitance change	Within ± 20 % of initial value		
Stability at low and high temperatures	25 °C	Dissipation factor	Within initial limit		
		Leakage current	Within initial limit		
		Capacitance change	Within ± 30 % of initial value		
	85 °C	Dissipation factor	Shall not exceed 120 % of initial limit		
		Leakage current	Shall not exceed 1000 % of initial value		
		Capacitance change	Within 0 % to +50 % of initial value		
	105 °C / 125 °C	Dissipation factor	Shall not exceed 150 % of initial limit		
		Leakage current	Shall not exceed 1000 % of initial limit		
	105 °C, 1.3 rated voltage,	Capacitance change	Within ± 20 % of initial value		
Surge voltage	125 °C, 1.3 x 2/3 rated voltage,	Dissipation factor	Within initial limit		
Surge voltage	1000 successive test cycles with 33 Ω at the rate	Leakage current	Shall not exceed 300 % of initial limit		
	of 30 s ON, 30 s OFF	ESR	Shall not exceed 300 % of initial limit		
		Capacitance change	Within ± 20 % of initial value		
Shock	MIL-STD-202, figure 1 of method 213,	Dissipation factor	Within initial limit		
(specified pulse)	condition F	Leakage current	Within initial limit		
(opcomed pales)		There shall be no mechanical or visual damage to capacitors post-conditioning.			
		Capacitance change	Within ± 20 % of initial value		
	MIL-STD-202, method 204, 5 g's for 20 min.,	Dissipation factor	Within initial limit		
Vibration	12 cycles each of 3 orientations.	Leakage current	Within initial limit		
	Test from 10 Hz to 2000 Hz.	There shall be no mechanical or visual damage to capacitors post-conditioning.			

PRODUCT INFORMATION						
Polymer Guide	www.vishay.com/doc?40076					
Moisture Sensitivity	www.vishay.com/doc?40135					
Infographic	www.vishay.com/doc?48084					
Sample Board	www.vishay.com/doc?48073					
FAQ						
Frequently Asked Questions	www.vishay.com/doc?42106					



Guide for Tantalum Solid Electrolyte Chip Capacitors With Polymer Cathode

INTRODUCTION

Tantalum electrolytic capacitors are the preferred choice in applications where volumetric efficiency, stable electrical parameters, high reliability, and long service life are primary considerations. The stability and resistance to elevated temperatures of the tantalum/tantalum oxide/manganese dioxide system make solid tantalum capacitors an appropriate choice for today's surface mount assembly technology.

Vishay Sprague has been a pioneer and leader in this field, producing a large variety of tantalum capacitor types for consumer, industrial, automotive, military, and aerospace electronic applications.

Tantalum is not found in its pure state. Rather, it is commonly found in a number of oxide minerals, often in combination with Columbium ore. This combination is known as "tantalite" when its contents are more than one-half tantalum. Important sources of tantalite include Australia, Brazil, Canada, China, and several African countries. Synthetic tantalite concentrates produced from tin slags in Thailand, Malaysia, and Brazil are also a significant raw material for tantalum production.

Electronic applications, and particularly capacitors, consume the largest share of world tantalum production. Other important applications for tantalum include cutting tools (tantalum carbide), high temperature super alloys, chemical processing equipment, medical implants, and military ordnance.

Vishay Sprague is a major user of tantalum materials in the form of powder and wire for capacitor elements and rod and sheet for high temperature vacuum processing.

THE BASICS OF TANTALUM CAPACITORS

Most metals form crystalline oxides which are non-protecting, such as rust on iron or black oxide on copper. A few metals form dense, stable, tightly adhering, electrically insulating oxides. These are the so-called "valve" metals and include titanium, zirconium, niobium, tantalum, hafnium, and aluminum. Only a few of these permit the accurate control of oxide thickness by electrochemical means. Of these, the most valuable for the electronics industry are aluminum and tantalum.

Capacitors are basic to all kinds of electrical equipment, from radios and television sets to missile controls and automobile ignitions. Their function is to store an electrical charge for later use.

Capacitors consist of two conducting surfaces, usually metal plates, whose function is to conduct electricity. They are separated by an insulating material or dielectric. The dielectric used in all tantalum electrolytic capacitors is tantalum pentoxide.

Tantalum pentoxide compound possesses high-dielectric strength and a high-dielectric constant. As capacitors are being manufactured, a film of tantalum pentoxide is applied to their electrodes by means of an electrolytic process. The film is applied in various thicknesses and at various voltages and although transparent to begin with, it takes on different colors as light refracts through it. This coloring occurs on the tantalum electrodes of all types of tantalum capacitors.

Rating for rating, tantalum capacitors tend to have as much as three times better capacitance/volume efficiency than aluminum electrolytic capacitors. An approximation of the capacitance/volume efficiency of other types of capacitors may be inferred from the following table, which shows the dielectric constant ranges of the various materials used in each type. Note that tantalum pentoxide has a dielectric constant of 26, some three times greater than that of aluminum oxide. This, in addition to the fact that extremely thin films can be deposited during the electrolytic process mentioned earlier, makes the tantalum capacitor extremely efficient with respect to the number of microfarads available per unit volume. The capacitance of any capacitor is determined by the surface area of the two conducting plates, the distance between the plates, and the dielectric constant of the insulating material between the plates.

COMPARISON OF CAPACITOR DIELECTRIC CONSTANTS					
DIELECTRIC	e DIELECTRIC CONSTANT				
Air or vacuum	1.0				
Paper	2.0 to 6.0				
Plastic	2.1 to 6.0				
Mineral oil	2.2 to 2.3				
Silicone oil	2.7 to 2.8				
Quartz	3.8 to 4.4				
Glass	4.8 to 8.0				
Porcelain	5.1 to 5.9				
Mica	5.4 to 8.7				
Aluminum oxide	8.4				
Tantalum pentoxide	26				
Ceramic	12 to 400K				

In the tantalum electrolytic capacitor, the distance between the plates is very small since it is only the thickness of the tantalum pentoxide film. As the dielectric constant of the tantalum pentoxide is high, the capacitance of a tantalum capacitor is high if the area of the plates is large:

$$C = \frac{eA}{t}$$

where

C = capacitance

e = dielectric constant

A = surface area of the dielectric

t = thickness of the dielectric

Tantalum capacitors contain either liquid or solid electrolytes. In solid electrolyte capacitors, a dry material (manganese dioxide) forms the cathode plate. A tantalum lead is embedded in or welded to the pellet, which is in turn connected to a termination or lead wire. The drawings show the construction details of the surface mount types of tantalum capacitors shown in this catalog.

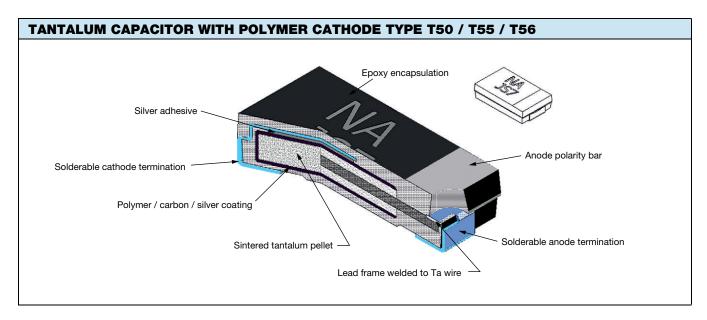


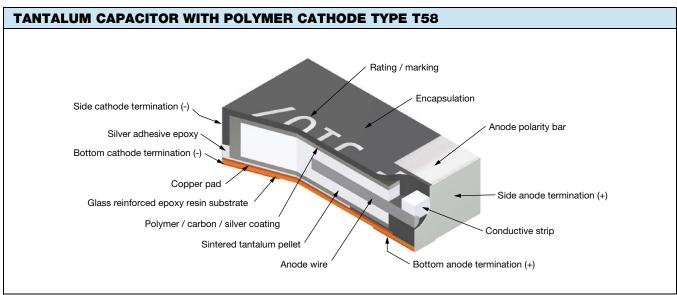
SOLID ELECTROLYTE POLYMER TANTALUM CAPACITORS

Solid electrolyte polymer capacitors utilize sintered tantalum pellets as anodes. Tantalum pentoxide dielectric layer is formed on the entire surface of anode, which is further impregnated with highly conductive polymer as cathode system.

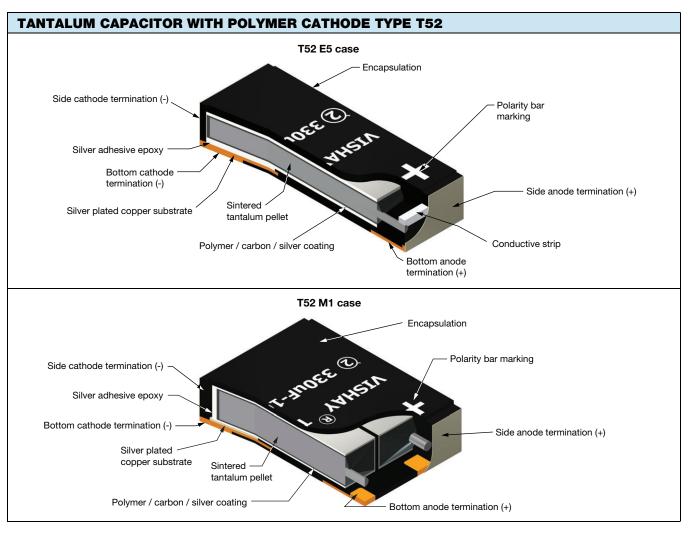
The conductive polymer layer is then coated with graphite, followed by a layer of metallic silver, which provides a conductive surface between the capacitor element and the outer termination (lead frame or other).

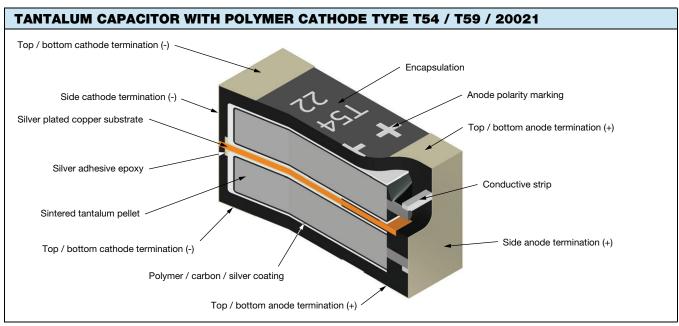
Molded chip polymer tantalum capacitor encases the element in plastic resins, such as epoxy materials. The molding compound has been selected to meet the requirements of UL 94 V-0 and outgassing requirements of ASTM E-595. After assembly, the capacitors are tested and inspected to assure long life and reliability. It offers excellent reliability and high stability for variety of applications in electronic devices. Usage of conductive polymer cathode system provides very low equivalent series resistance (ESR), which makes the capacitors particularly suitable for high frequency applications.











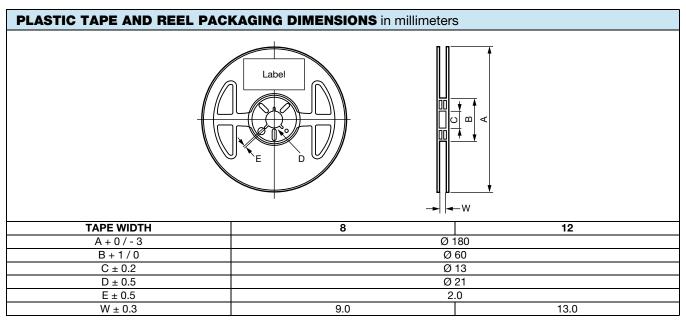


POLYMER CAPACITORS - MOLDED CASE					
SERIES	T50, T55, T56				
PRODUCT IMAGE	THE THE PARTY OF T				
TYPE	VPolyTan [™] , molded case, high performance polymer				
FEATURES	High performance				
TEMPERATURE RANGE	-55 °C to +105 °C / +125 °C				
CAPACITANCE RANGE	3.3 μF to 1000 μF				
VOLTAGE RANGE	2.5 V to 63 V				
CAPACITANCE TOLERANCE	± 20 %				
LEAKAGE CURRENT	0.1 CV				
DISSIPATION FACTOR	8 % to 10 %				
ESR	$6~\text{m}\Omega$ to $500~\text{m}\Omega$				
CASE SIZES	J, P, A, T, B, Z, V, D, C				
TERMINATION FINISH	Cases J, P, C: 100 % tin Case A, T, B, Z, V, D: Ni / Pd / Au				

POLYMER CAPACITORS - LEADFRAMELESS MOLDED CASE									
SERIES	T52	T58	T59	T54	20021				
PRODUCT IMAGE		E 1/07			\$ 2 m				
TYPE	vPolyTan TM polymer surface mount chip capacitors, low profile, leadframeless molded type	vPolyTan TM polymer surface mount chip capacitors, low profile, leadframeless molded type vPolyTan TM polymer surface mount chip capacitors, low ESR, leadframeless molded type vPolyTan TM polymer surface mount chip capacitors, low ESR, leadframeless molded type hi-rel commer		vPolyTan TM polymer surface mount chip capacitors, low ESR, leadframeless molded type, hi-rel commercial off-the-shelf (COTS)	vPolyTan TM polymer surface mount chip capacitors, low ESR, leadframeless molded type, DLA approved				
FEATURES	Low profile	Small case size	Multianode	Hi-rel COTS, multianode	Multianode				
TEMPERATURE RANGE	-55 °C to +105 °C	-55 °C to +105 °C	-55 °C to +105 °C	-55 °C to +125 °C	-55 °C to +125 °C				
CAPACITANCE RANGE	47 μF to 1500 μF	1 μF to 330 μF	15 μF to 470 μF	15 μF to 470 μF (discrete capacitors) 30 μF to 2800 μF (stacked capacitors)	15 μF to 470 μF				
VOLTAGE RANGE	10 V to 35 V	6.3 V to 35 V	16 V to 75 V	16 V to 75 V	16 V to 63 V				
CAPACITANCE TOLERANCE	± 20 %	± 20 %	± 10 %, ± 20 %	± 20 %	± 20 %				
LEAKAGE CURRENT			0.1 CV						
DISSIPATION FACTOR	10 %	8 % to 14 %	12 %	12 %	10 %				
ESR	25 m Ω to 55 m Ω	50 m Ω to 500 m Ω	25 m Ω to 150 m Ω	5 m Ω to 150 m Ω	25 m Ω to 150 m Ω				
CASE SIZES	E5, M1, M9, B2	MM, W0, W9, A0, BB	EE	EE, E2, E3, E4, E6	EE				
TERMINATION	100	% tin	100 % tin	; tin / lead	Tin / lead				

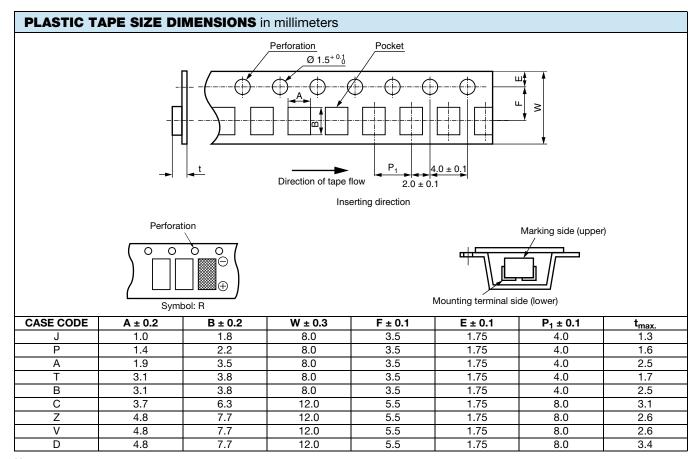


MOLDED CAPACITORS, T50 / T55 / T56 TYPES



Note

· A reel diameter of 330 mm is also applicable

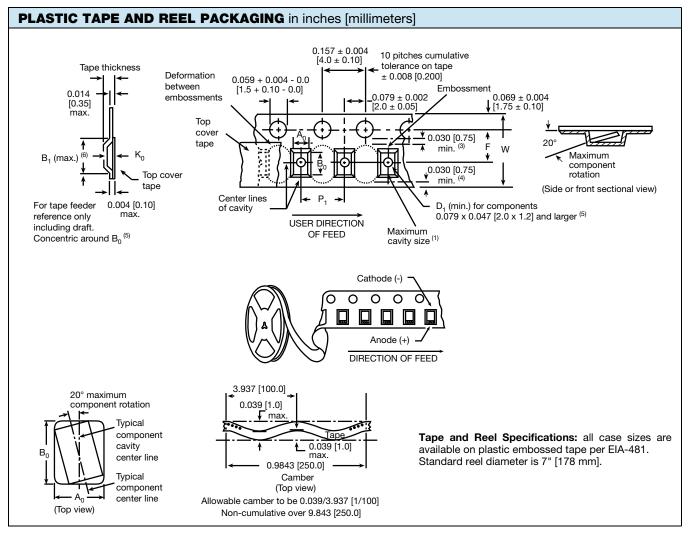


Note

A reel diameter of 330 mm is also applicable



LEADFRAMELESS MOLDED CAPACITORS, ALL TYPES



Notes

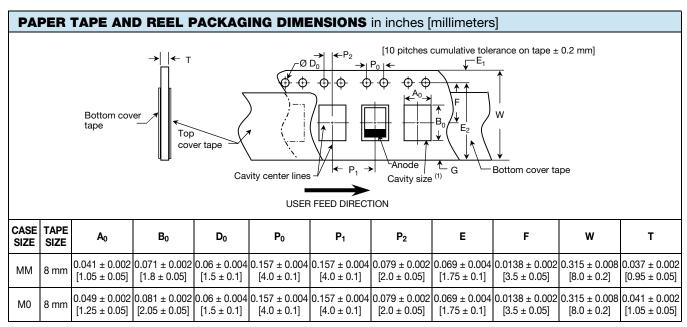
- Metric dimensions will govern. Dimensions in inches are rounded and for reference only
- (1) A₀, B₀, K₀, are determined by the maximum dimensions to the ends of the terminals extending from the component body and / or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A₀, B₀, K₀) must be within 0.002" (0.05 mm) minimum and 0.020" (0.50 mm) maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20°
- (2) Tape with components shall pass around radius "R" without damage. The minimum trailer length may require additional length to provide "R" minimum for 12 mm embossed tape for reels with hub diameters approaching N minimum
- (3) This dimension is the flat area from the edge of the sprocket hole to either outward deformation of the carrier tape between the embossed cavities or to the edge of the cavity whichever is less
- (4) This dimension is the flat area from the edge of the carrier tape opposite the sprocket holes to either the outward deformation of the carrier tape between the embossed cavity or to the edge of the cavity whichever is less
- (5) The embossed hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location shall be applied independent of each other
- (6) B₁ dimension is a reference dimension tape feeder clearance only



CARRIER 1	APE DIME	NSIONS in in	ches [millime	eters]			
CASE CODE	TAPE SIZE	B ₁ (MAX.) ⁽¹⁾	D ₁ (MIN.)	F	K ₀ (MAX.)	P ₁	w
E5	12 mm	0.329 [8.35]	0.059 [1.5]	0.217 ± 0.002 [5.50 ± 0.05]	0.071 [1.8]	0.315 ± 0.004 [8.0 ± 0.10]	0.476 ± 0.008 [12.1 ± 0.20]
MM ⁽²⁾	8 mm	0.075 [1.91]	0.02 [0.5]	0.138 [3.5]	0.043 [1.10]	0.157 [4.0]	0.315 [8.0]
M1, M9	12 mm	0.32 [8.2]	0.059 [1.5]	0.217 ± 0.002 [5.5 ± 0.05]	0.094 [2.39]	0.315 ± 0.04 [8.0 ± 1.0]	0.472 + 0.012 / - 0.004 [12.0 + 0.3 / - 0.10]
W9	8 mm	0.126 [3.20]	0.030 [0.75]	0.138 [3.5]	0.045 [1.15]	0.157 [4.0]	0.315 [8.0]
W0	8 mm	0.126 [3.20]	0.030 [0.75]	0.138 [3.5]	0.045 [1.15]	0.157 [4.0]	0.315 [8.0]
A0	8 mm	-	0.02 [0.5]	0.138 [3.5]	0.049 [1.25]	0.157 [4.0]	0.315 [8.0]
BB	8 mm	0.157 [4.0]	0.039 [1.0]	0.138 [3.5]	0.087 [2.22]	0.157 [4.0]	0.315 [8.0]
EE	12 mm	0.32 [8.2]	0.059 [1.5]	0.217 ± 0.002 [5.5 ± 0.05]	0.175 [4.44]	0.315 ± 0.04 [8.0 ±1.0]	0.472 + 0.012 / - 0.004 [12.0 + 0.3 / - 0.10]
B2	8 mm	0.157 [4.0]	0.039 [1.0]	0.138 [3.5]	0.057 [1.45]	0.157 [4.0]	0.315 [8.0]

Notes

- (1) For reference only
- (2) Standard packaging of MM case is with paper tape. Plastic tape is available per request



Note

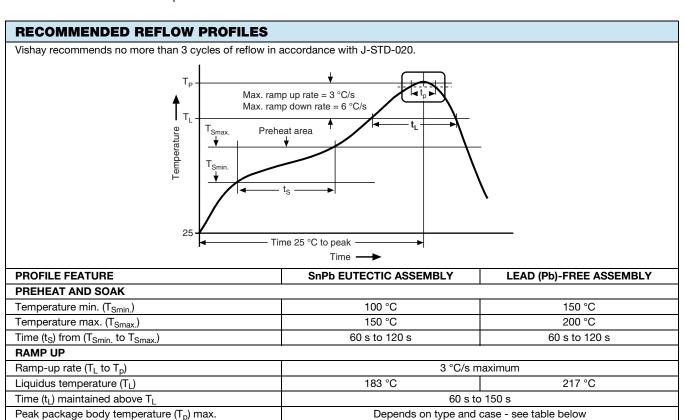
⁽¹⁾ A₀, B₀ are determined by the maximum dimensions to the ends of the terminals extending from the component body and / or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A₀, B₀) must be within 0.002" (0.05 mm) minimum and 0.020" (0.50 mm) maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20°



PACKING AND STORAGE

Polymer capacitors meet moisture sensitivity level rating (MSL) of 3 or 4 as specified in IPC/JEDEC® J-STD-020 and are dry packaged in moisture barrier bags (MBB) per J-STD-033. MSL for each particular family is defined in the datasheet - either in "Features" section or "Standard Ratings" table. Level 3 specifies a floor life (out of bag) of 168 hours and level 4 specifies a floor life of 72 hours at 30 °C maximum and 60 % relative humidity (RH). Unused capacitors should be re-sealed in the MBB with fresh desiccant. A moisture strip (humidity indicator card) is included in the bag to assure dryness. To remove excess moisture, capacitors can be dried at 40 °C (standard "dry box" conditions).

For detailed recommendations please refer to J-STD-033.



PEAK PACKAGE BODY TEMPERATURE (T _p) MAXIMUM				
TYPE	CASE CODE	PEAK PACKAGE BODY TEMPERATURE (T _P) MAX.		
		SnPb EUTECTIC ASSEMBLY	LEAD (Pb)-FREE ASSEMBLY	
T55	J, P, A, T, B, C, Z, V, D	n/a	260 °C	
T52	E5, M1, M9, B2		260 °C	
T58	MM, M0, W9, W0, A0, BB		260 °C	
T50	D		260 °C	
T56	D		250 °C	
T59	EE	220 °C	250 °C	
T54	EE, E2, E3, E4, E6	220 °C	250 °C	
20021	EE	220 °C	n/a	

20 s

6 min maximum

6 °C/s maximum

8 min maximum

Notes

RAMP DOWN

Ramp-down rate (Tp to TL)

Time from 25 °C to peak temperature

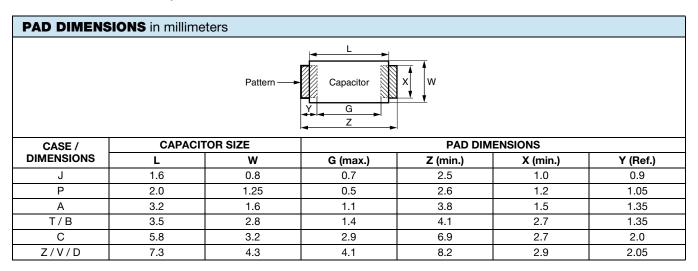
- T50, T52, T55, T56, and T58 capacitors are process sensitive. PSL classification to JEDEC J-STD-075: R4G

Time (t_p) within 5 °C of the peak max. temperature

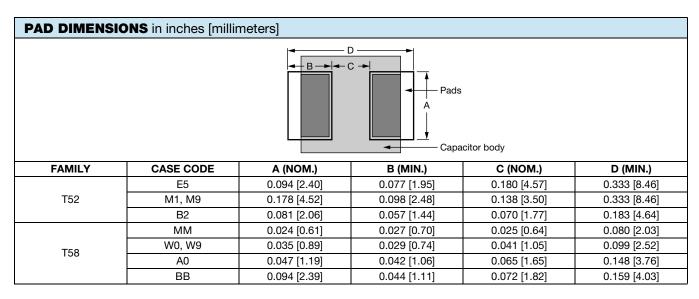
T54 and T59 capacitors with 100 % tin termination are process sensitive. PSL classification to JEDEC J-STD-075: R6G



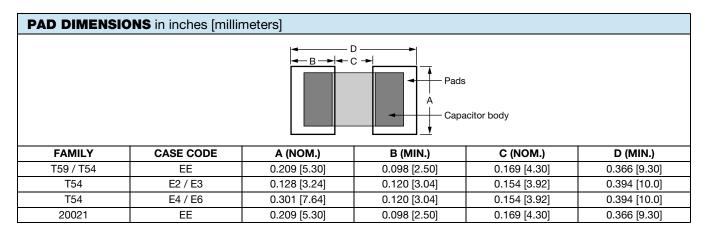
MOLDED CAPACITORS, T50 / T55 / T56 TYPES



LEADFRAMELESS MOLDED CAPACITORS T52 / T58



LEADFRAMELESS MOLDED CAPACITORS T59 / T54 / 20021





GUIDE TO APPLICATION

 AC Ripple Current: the maximum allowable ripple current shall be determined from the formula:

$$I_{RMS} = \sqrt{\frac{P}{R_{ESR}}}$$

where.

P = power dissipation in W at +45 °C as given in the tables in the product datasheets.

R_{ESR} = the capacitor equivalent series resistance at the specified frequency.

AC Ripple Voltage: the maximum allowable ripple voltage shall be determined from the formula:

$$V_{RMS} = Z \sqrt{\frac{P}{R_{ESR}}}$$

or, from the formula:

$$V_{RMS} = I_{RMS} \times Z$$

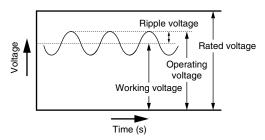
where,

P = power dissipation in W at +45 °C as given in the tables in the product datasheets.

R_{ESR} = The capacitor equivalent series resistance at the specified frequency.

Z = The capacitor impedance at the specified frequency.

2.1 The tantalum capacitors must be used in such a condition that the sum of the working voltage and ripple voltage peak values does not exceed the rated voltage as shown in figure below.



3. **Temperature Derating:** power dissipation is affected by the heat sinking capability of the mounting surface. If these capacitors are to be operated at temperatures above +45 °C, the permissible ripple current (or voltage) shall be calculated using the derating coefficient as shown in the table below:

MAXIMUM RIPPLE CURRENT TEMPERATURE DERATING FACTOR			
≤ 45 °C	1.0		
55 °C	0.8		
85 °C	0.6		
105 °C	0.4		
125 °C	0.25		

4. **Reverse Voltage:** the capacitors are not intended for use with reverse voltage applied. However, they are capable of withstanding momentary reverse voltage peaks, which must not exceed the following values:

At 25 $^{\circ}\text{C}$: 10 % of the rated voltage or 1 V, whichever is smaller.

At 85 $^{\circ}\text{C}\text{:}$ 5 % of the rated voltage or 0.5 V, whichever is smaller.

At 105 $^{\circ}\text{C}\text{: }3$ % of the rated voltage or 0.3 V, whichever is smaller.

5. Mounting Precautions:

- 5.1 **Soldering:** capacitors can be attached by conventional soldering techniques; vapor phase, convection reflow, infrared reflow, wave soldering, and hot plate methods. The soldering profile charts show recommended time / temperature conditions for soldering. Preheating is recommended. The recommended maximum ramp rate is 2 °C per s. Attachment with a soldering iron is not recommended due to the difficulty of controlling temperature and time at temperature. The soldering iron must never come in contact with the capacitor. For details see www.vishay.com/doc?40214.
- 5.2 Limit Pressure on Capacitor Installation with Mounter: pressure must not exceed 4.9 N with a tool end diameter of 1.5 mm when applied to the capacitors using an absorber, centering tweezers, or similar (maximum permitted pressurization time: 5 s). An excessively low absorber setting position would result in not only the application of undue force to the capacitors but capacitor and other component scattering, circuit board wiring breakage, and / or cracking as well, particularly when the capacitors are mounted together with other chips having a height of 1 mm or less.

5.3 Flux Selection

- 5.3.1 Select a flux that contains a minimum of chlorine and amine.
- 5.3.2 After flux use, the chlorine and amine in the flux remain must be removed.
- 5.4 Cleaning After Mounting: the following solvents are usable when cleaning the capacitors after mounting. Never use a highly active solvent.
 - Halogen organic solvent (HCFC225, etc.)
 - Alcoholic solvent (IPA, ethanol, etc.)
 - Petroleum solvent, alkali saponifying agent, water,

Circuit board cleaning must be conducted at a temperature of not higher than 50 °C and for an immersion time of not longer than 30 minutes. When an ultrasonic cleaning method is used, cleaning must be conducted at a frequency of 48 kHz or lower, at an vibrator output of 0.02 W/cm³, at a temperature of not higher than 40 °C, and for a time of 5 minutes or shorter.

Notes

- Care must be exercised in cleaning process so that the mounted capacitor will not come into contact with any cleaned object or the like or will not get rubbed by a stiff brush or similar. If such precautions are not taken particularly when the ultrasonic cleaning method is employed, terminal breakage may occur
- When performing ultrasonic cleaning under conditions other than stated above, conduct adequate advance checkout



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